

## **Best Paper Award**

### **The Best Paper Award was given to:**

Michael Schaffner, Pascal Hager, Lukas Cavigelli, Pierre Greisen, Frank K. Gürkaynak, and Hubert Kaeslin

A Real-Time 720p Feature Extraction Core Based on Semantic Kernels Binarized

### **Other best paper nominees were:**

- Mohand Bentobache, Ahcène Bounceur, Reinhardt Euler, Yann Kieffer, and Salvador Mir  
New Techniques for Selecting Test Frequencies for Linear Analog Circuits
- Seogoo Lee and Andreas Gerstlauer  
Fine Grain Word Length Optimization for Dynamic Precision Scaling in DSP Systems
- Sotiris Thomas, Kyprianos Papadimitriou, and Apostolos Dollas  
Architecture and Implementation of Real-Time 3D Stereo Vision on a Xilinx FPGA
- Nathaniel A. Conos, Saro Meguerdichian, and Miodrag Potkonjak  
Gate Sizing in the Presence of Switching Activity and Input Vector Control
- Abdulkadir Akin, Ipek Baz, Luis Manuel Gaemperle, Alexandre Schmid, and Yusuf Leblebici  
Compressed Look-Up Table based Real-Time Rectification Hardware

## **PhD Forum Best Presentation Award**

### **The PhD Forum Best Presentation Award was given to:**

Upasna Vishnoi

Systematically and Quantitatively Optimized Area- and Energy-Efficient Matrix-Decomposition Accelerators

### **Runner-ups were:**

- Ioannis Vourkas  
Design and Development of Nano-Electronic Circuits and Architectures With Memristive Devices
- Tiansheng Zhang  
Resource Management Design in 3D-Stacked Multicore Systems for Improving Energy Efficiency